

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	flash\$3 adj bounc\$3 adj ball\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:23
L2	514	flash\$3 near4 ball\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:26
L3	8	2 with bounc\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:23
L4	4	2 with (processor\$1 or microprocessor\$1 or CPU\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:45
L7	71	((light or solar) adj power\$2) near3 calculator\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:33
L8	4	autonomous\$2 near2 startup	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:37
L9	2	autonomous\$2 adj startup	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:37
L10	73	autonomous\$2 adj start\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:38
L11	6	10 with (processor\$1 or microprocessor\$1 or CPU\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:43
L12	2879	embedded adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:49

L14	618	12 with (processor\$1 or microprocessor\$1 or CPU\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:43
L15	0	14 with 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:44
L16	0	14 with 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:44
L17	0	14 same 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:44
L18	52069	(initializ\$5 or start\$3) near4 (processor\$1 or microprocessor\$1 or CPU\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:47
L19	0	18 with 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:46
L20	10513	(initializ\$5 near3 program)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:48
L21	2	20 with 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:48
L22	6062	embedded near2 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:49
L23	6	22 with 20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 07:50
L24	13	("4947477").URPN.	USPAT	OR	OFF	2004/11/24 07:51

L25	9483	embedded near3 (processor\$1 or microprocessor\$1 or CPU\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:00
L26	2366	L25 with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:00
L28	1	26 with 20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:01
L29	17726	(multi adj chip adj module\$1) or MCM\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:02
L30	0	29 with 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:03
L31	0	29 same 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:04
L32	53781	(application adj specific adj integrated adj circuit\$1) or ASIC\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:04
L33	74	32 with 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:20
L37	656	instrument\$5 adj controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:20
L38	0	37 with 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:22

L39	7	22 with 18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 08:22
L40	5	("5974528").URPN.	USPAT	OR	OFF	2004/11/24 08:28
L41	13	("5293424"   "5442704"   "5644781"   "5682027"   "5687345"   "5771191"   "5828911"   "5881295"   "5905921"   "5923838"   "5974528"   "6038635"   "6075941").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/24 08:34
L42	1952	((on adj chip) or (single adj chip)) adj2 (microcomputer\$1 or (micro adj computer))	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/24 08:38
L43	13307	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/24 08:38
L44	0	42 with 43	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/24 08:38
L45	0	42 same 43	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/24 08:38
L46	2070	((on adj chip) or (single adj chip)) near2 (microcomputer\$1 or (micro adj computer))	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/24 08:40
L47	0	46 same 43	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/24 08:40

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L1	2879	embedded adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 16:31
L2	62244	(non adj volatile) adj memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 16:32
L3	957084	(processor or microprocessor or (micro adj processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 16:32
L4	10046213	(includ\$3 or hav\$3 or compris\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 16:34
L5	134720	3 near1 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 16:40
L6	14	5 near1 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 16:39
L7	157	5 near1 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 16:40
L8	144	5 adj 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 16:40
L9	51941	3 adj 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 16:41
L10	45	9 adj 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/24 16:41